

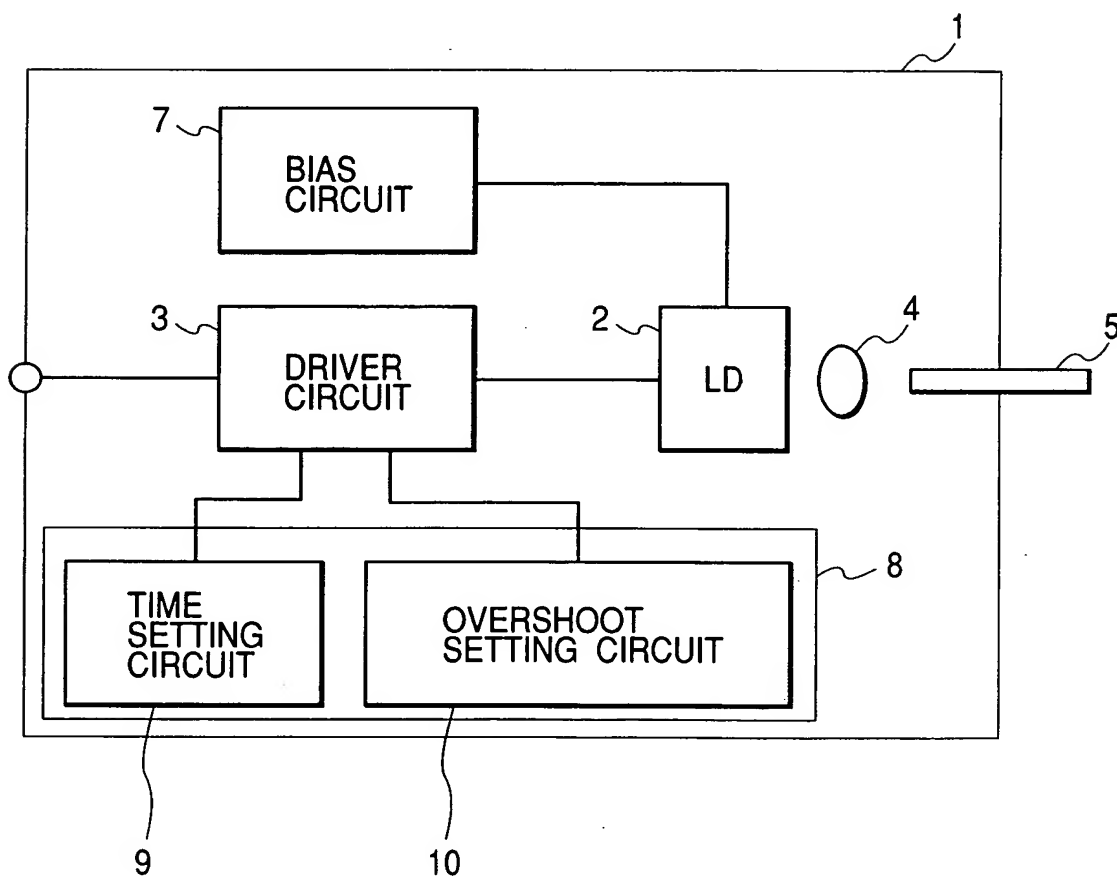
**FIG. 1**

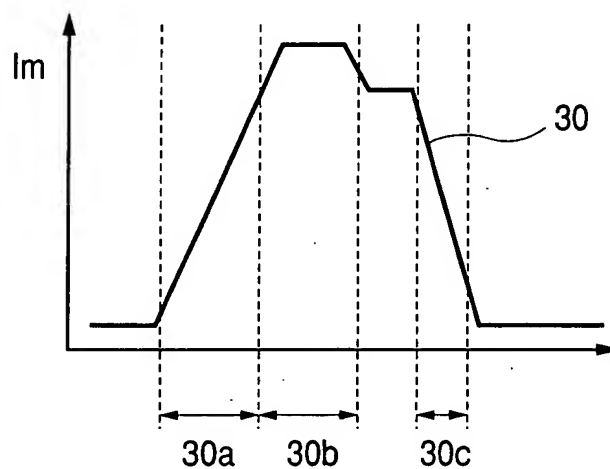
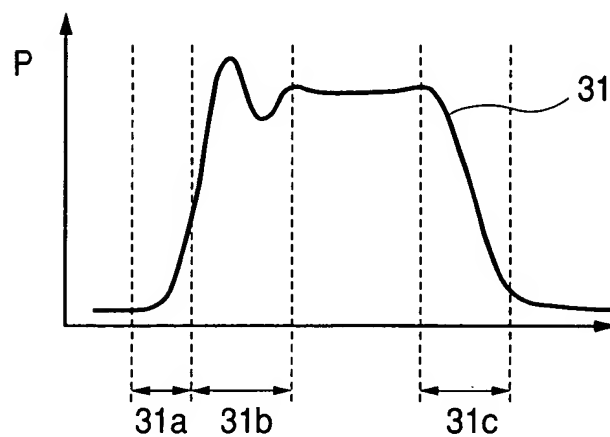
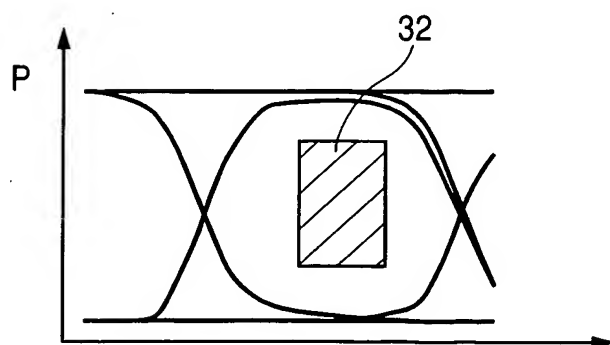
FIG. 2(a)**FIG. 2(b)****FIG. 2(c)**

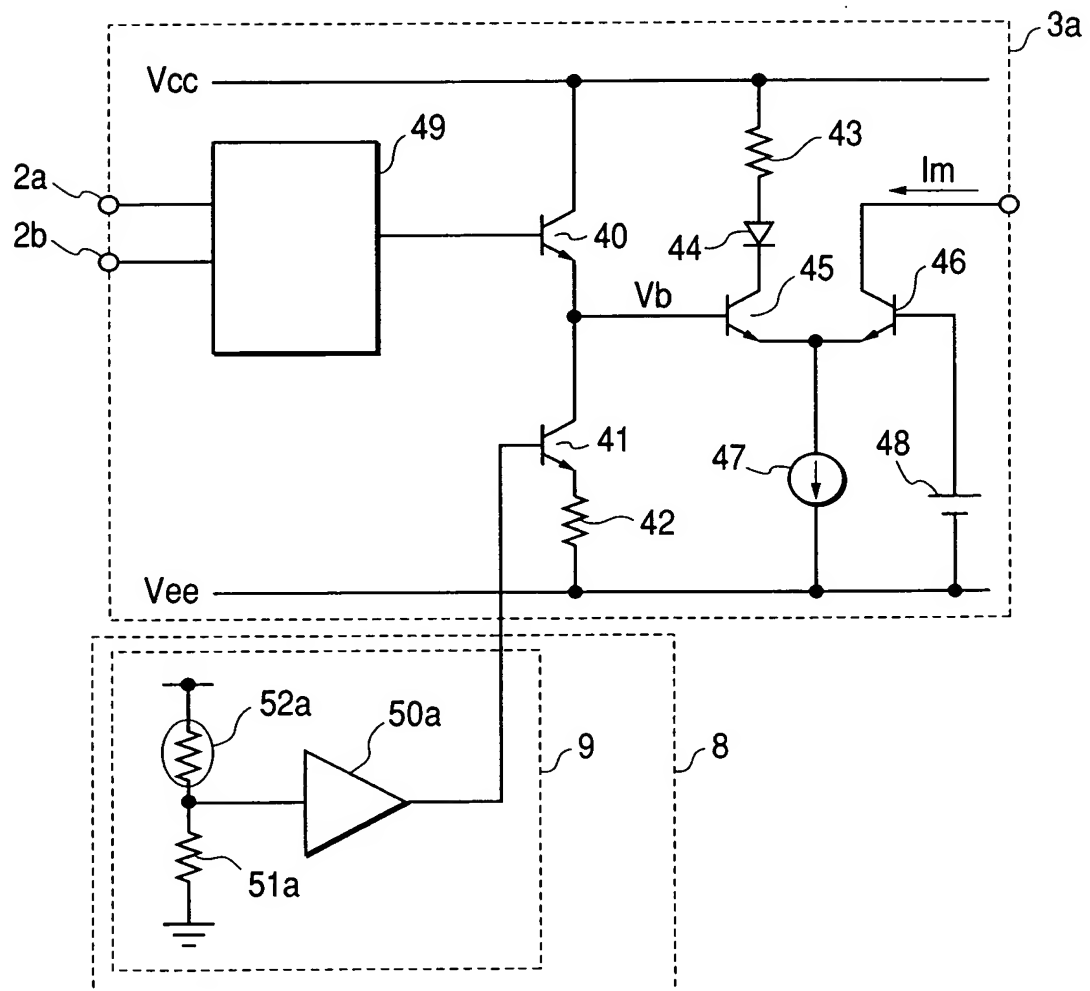
FIG. 3

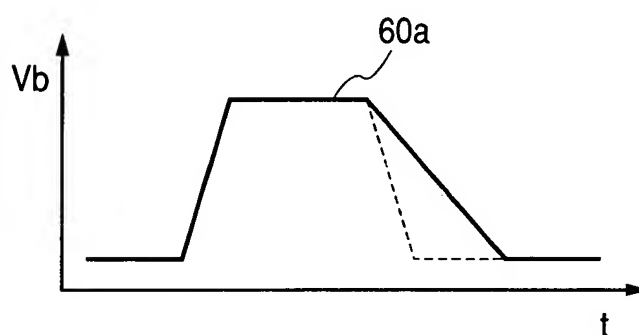
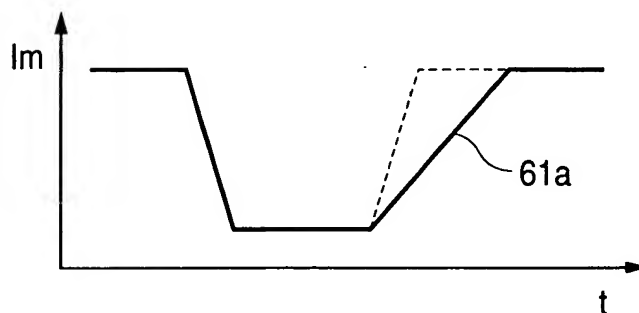
FIG. 4(a)*FIG. 4(b)*

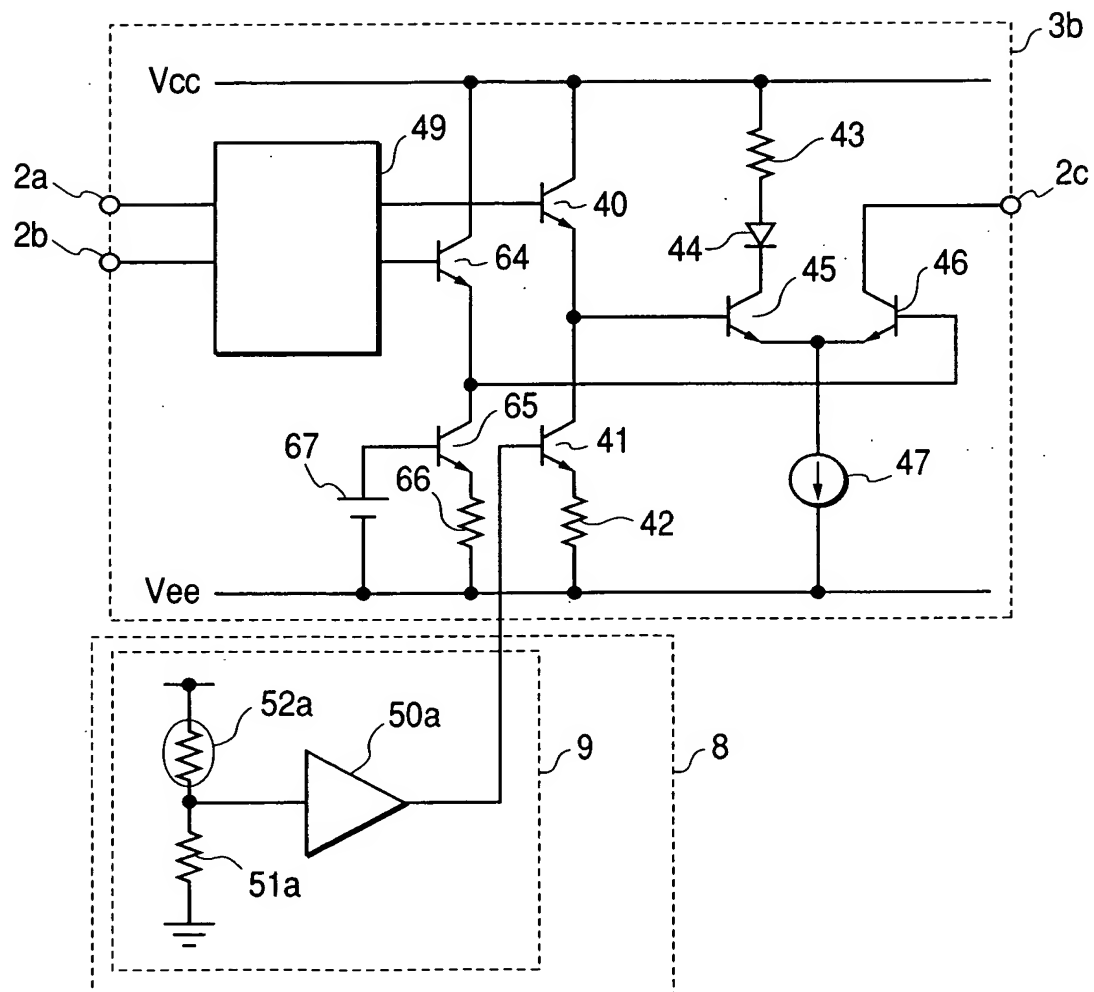
FIG. 5

Figure 1 is a schematic diagram of a multi-stage amplifier circuit. The circuit is divided into three main sections: 2c (input and first stage), 3c (second and third stages), and 9 (feedback network). Section 2c includes input terminals 2a and 2b, a Vee supply, and a network of transistors 49, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, and 80. Section 3c includes a Vcc supply, a network of transistors 43, 44, 45, 46, and 47, and current sources 81 and 82. Section 9 includes a feedback network with resistors 51a and 52a, and an op-amp 50a. The circuit is powered by Vcc and Vee supplies.

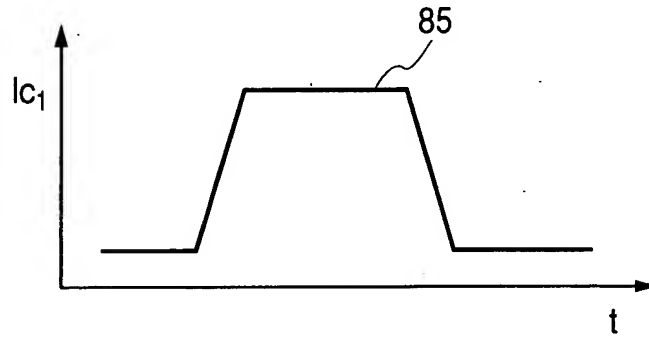
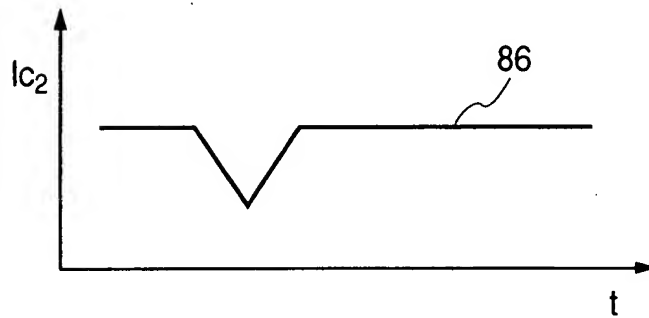
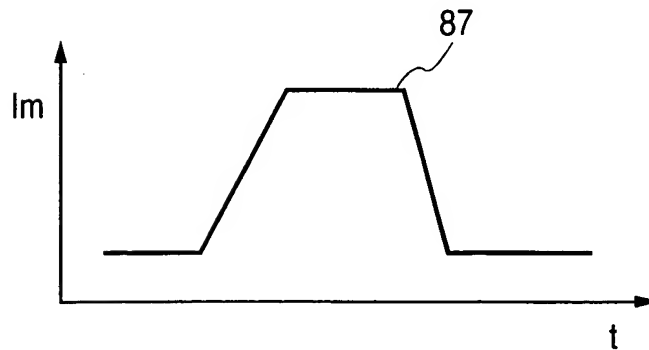
FIG. 7(a)**FIG. 7(b)****FIG. 7(c)**

FIG. 8

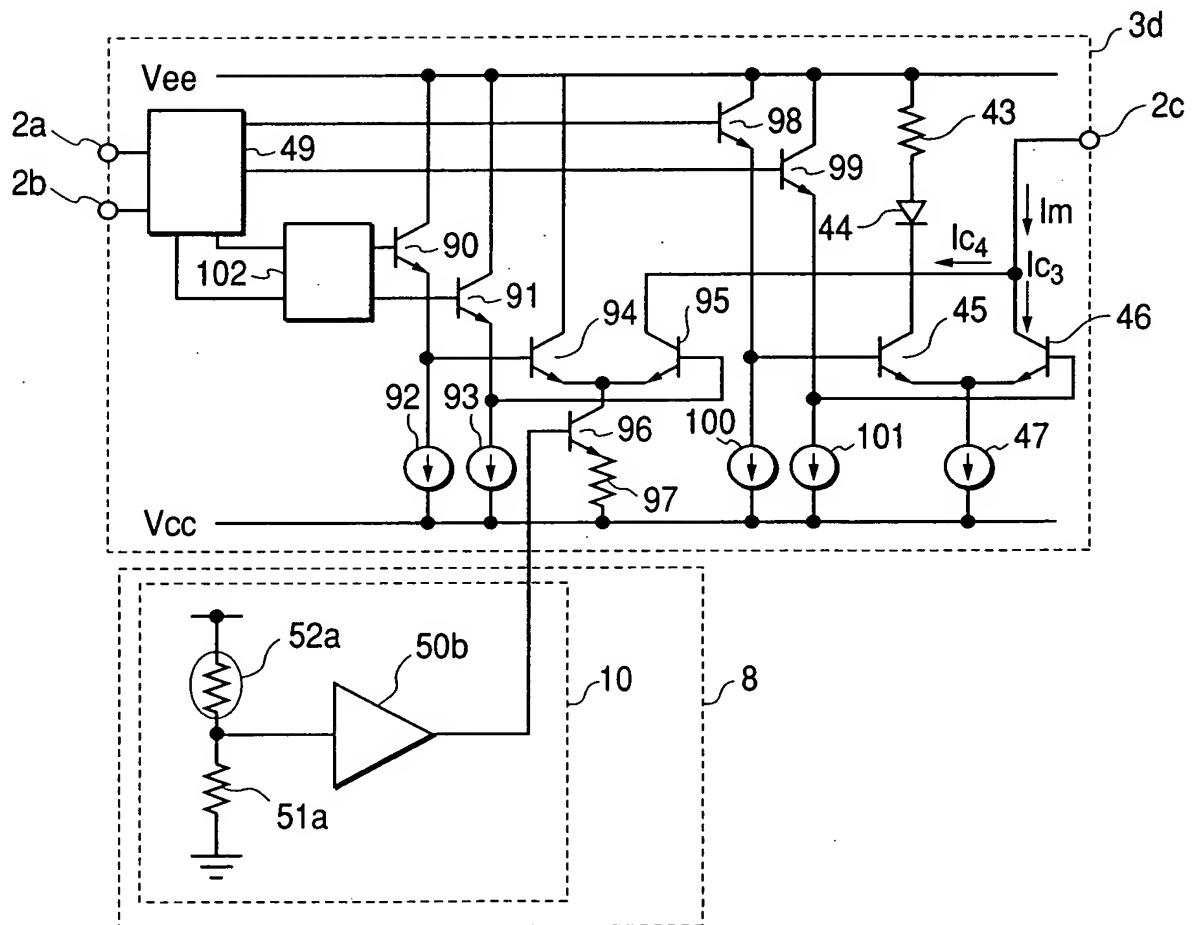
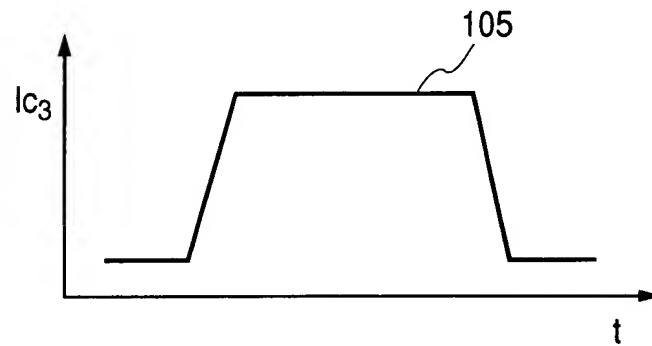
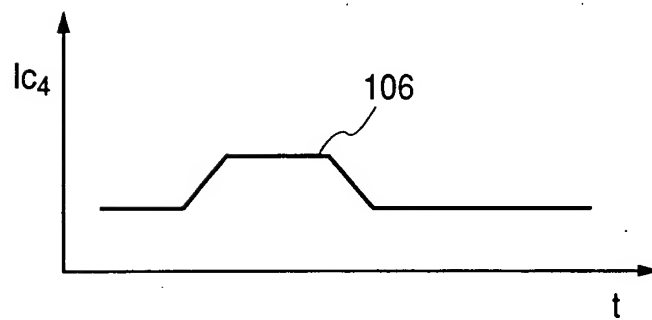
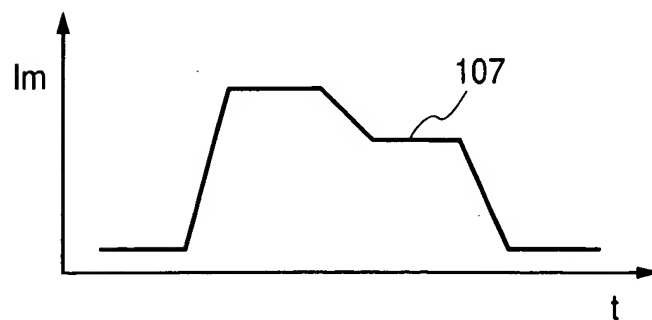


FIG. 9(a)**FIG. 9(b)****FIG. 9(c)**

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Application No. 10/642,373

"Directly Modulated Optical Module and Method for Driving Semiconductor Laser Included Therein"

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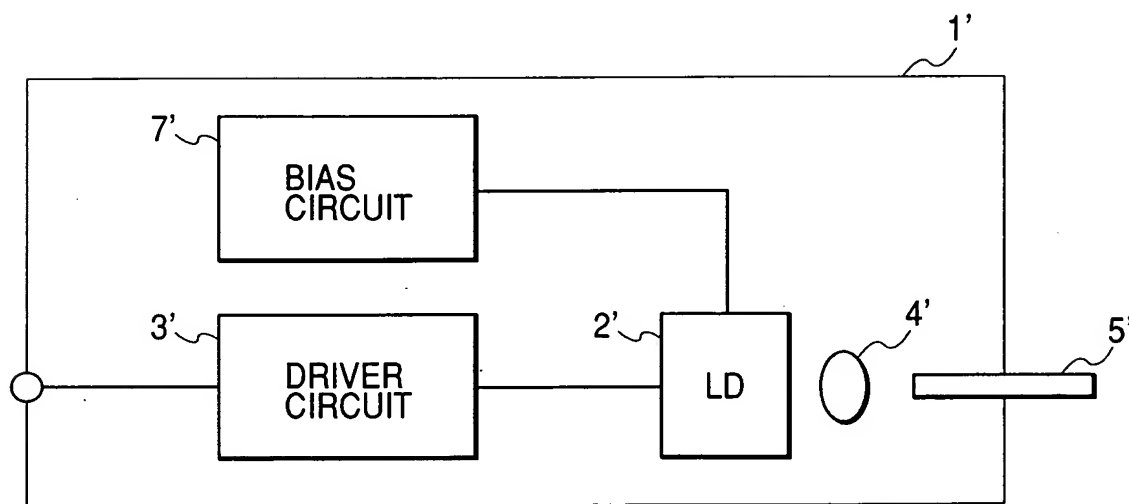
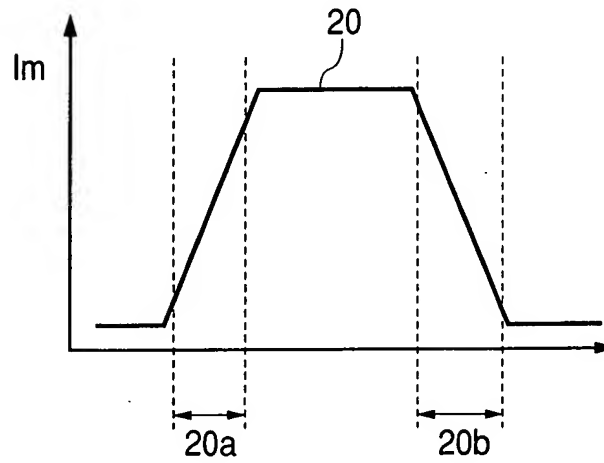
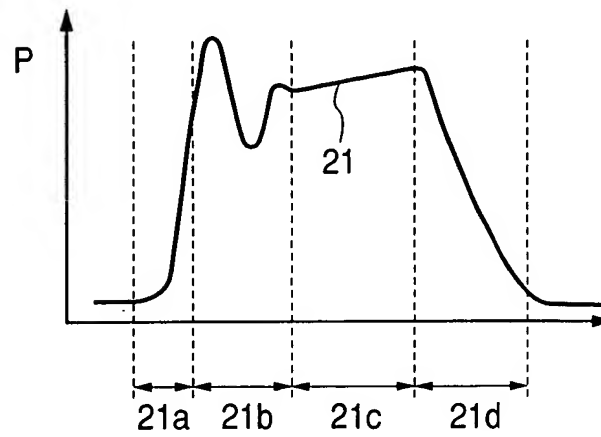
FIG. 10

FIG. 11(a)**FIG. 11(b)****FIG. 11(c)**